

ABSTRACT OF THE DISCLOSURE

An array substrate for use in an in-plane switching liquid crystal display device includes a gate line on a substrate; a data line crossing the gate line to define a pixel region having an aperture area; a thin film transistor disposed at one corner of the pixel region and connected to the gate line and the data line, the thin film transistor including a semiconductor layer; a common line spaced apart from and substantially parallel to the gate line; a common electrode extending from the common line and including a plurality of common electrode patterns, wherein an outermost portion of the common electrode pattern is substantially rectangle shaped within the pixel region and has a substantially circular opening in the middle thereof; a capacitor electrode overlapping the rectangle shaped common electrode pattern, the capacitor electrode connected to the thin film transistor; a pixel connecting line substantially parallel to the data line in the pixel region and connected to the capacitor electrode; and a pixel electrode disposed within the substantially circular opening, extending from the pixel connecting line and including a plurality of pixel electrode patterns; wherein an innermost pixel electrode pattern has a substantially circular shape and other pixel electrode patterns are patterned to have circular bands, and wherein the aperture area is circular banded shaped.